



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor application of: Akira YOSHIDA et al.

Appl. No. 10/061,302

Confirmation No. 2370

Filed: February 4, 2002

For: SIGNAL COMPENSATION  
CIRCUIT AND DEMODULATING  
CIRCUIT WITH HIGH-SPEED  
AND LOW-SPEED FEEDBACK  
LOOPS

Art Unit: 2631

Examiner:

Atty. Docket No. 31869-178004

Customer No.



26694

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**Attention: BOX PATENT APPLICATION**

Sir:

Submitted herewith is a marked up copy of the original filing receipt with a request that a corrected filing receipt be issued reflecting the correct title as follows:

**--SIGNAL COMPENSATION CIRCUIT AND DEMODULATING CIRCUIT WITH  
HIGH-SPEED AND LOW-SPEED FEEDBACK LOOPS--.**

Respectfully submitted,

Date: MARCH 21, 2002

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/061,302	02/04/2002	2631	740	31869-178004	11	11	2

CONFIRMATION NO. 2370

**FILING RECEIPT**



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**COMPUTER UPDATED**

Venable  
P.O. Box 34385  
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Date Mailed: 03/05/2002

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**Applicant(s)**

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Akira Horikawa, Chiba, JAPAN;  
Shuichi Matsumoto, Chiba, JAPAN;

**Domestic Priority data as claimed by applicant**

**Foreign Applications**

JAPAN 2001-071381 03/14/2001

**If Required, Foreign Filing License Granted 03/05/2002**

**Projected Publication Date: 09/19/2002**

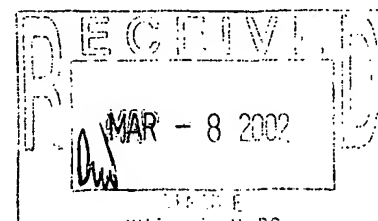
**Non-Publication Request: No**

**Early Publication Request: No**

**Title**

Signal compensation circuit and demodulating circuit with high-speed and low-speed ~~feedback~~ loops

**Preliminary Class**



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Bib Data Sheet

CONFIRMATION NO. 2370

<b>SERIAL NUMBER</b> 10/061,302	<b>FILING DATE</b> 02/04/2002 <b>RULE</b>	<b>CLASS</b> 375	<b>GROUP ART UNIT</b> 2631	<b>ATTORNEY DOCKET NO.</b> 31869-178004
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## APPLICANTS

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Akira Horikawa, Chiba, JAPAN;  
Shuichi Matsumoto, Chiba, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\*

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 2001-071381 03/14/2001

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\*\* 03/05/2002

Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 11	<b>TOTAL CLAIMS</b> 11	<b>INDEPENDENT CLAIMS</b> 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

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## TITLE

Signal compensation circuit and demodulating circuit with high-speed and low-speed feedback loops

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